

Fig. 1

# 1. CONTROLLER

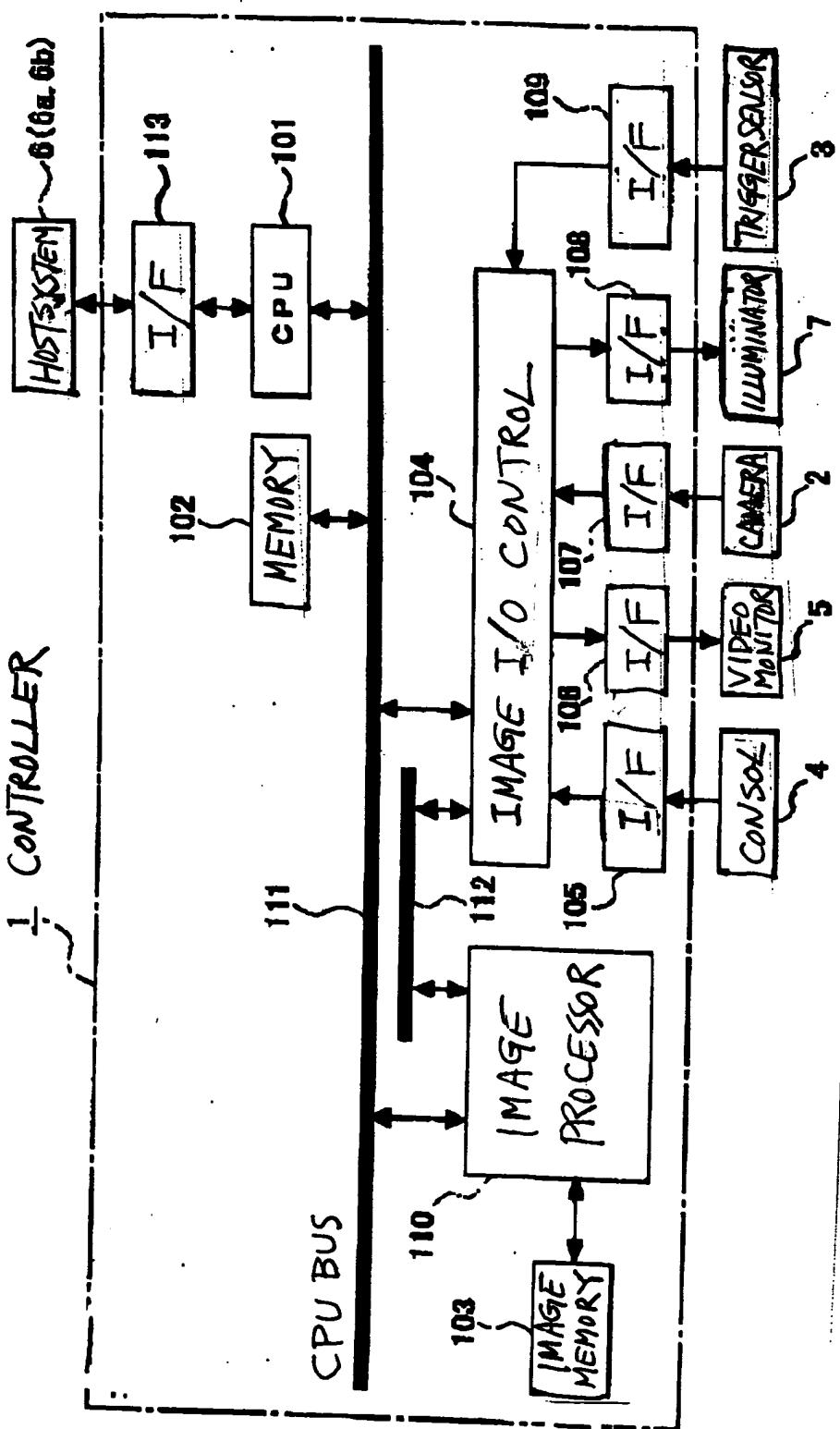


Fig. 2

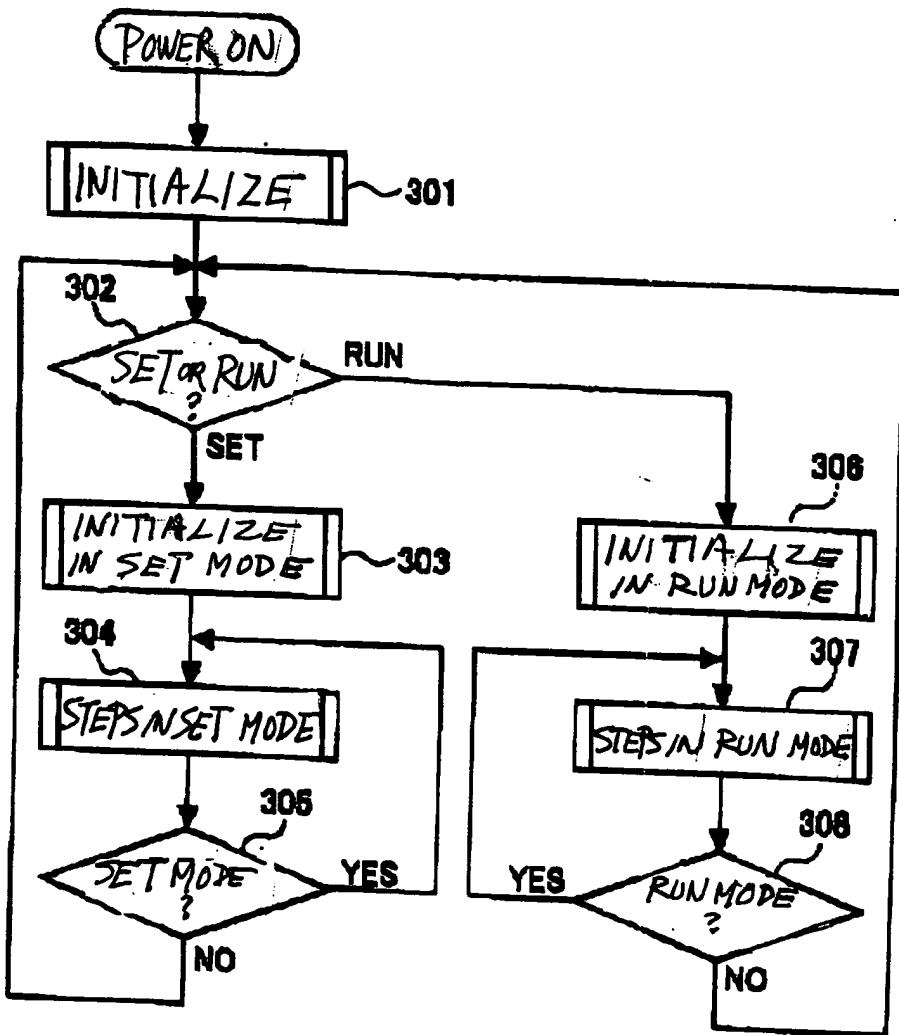


Fig. 3

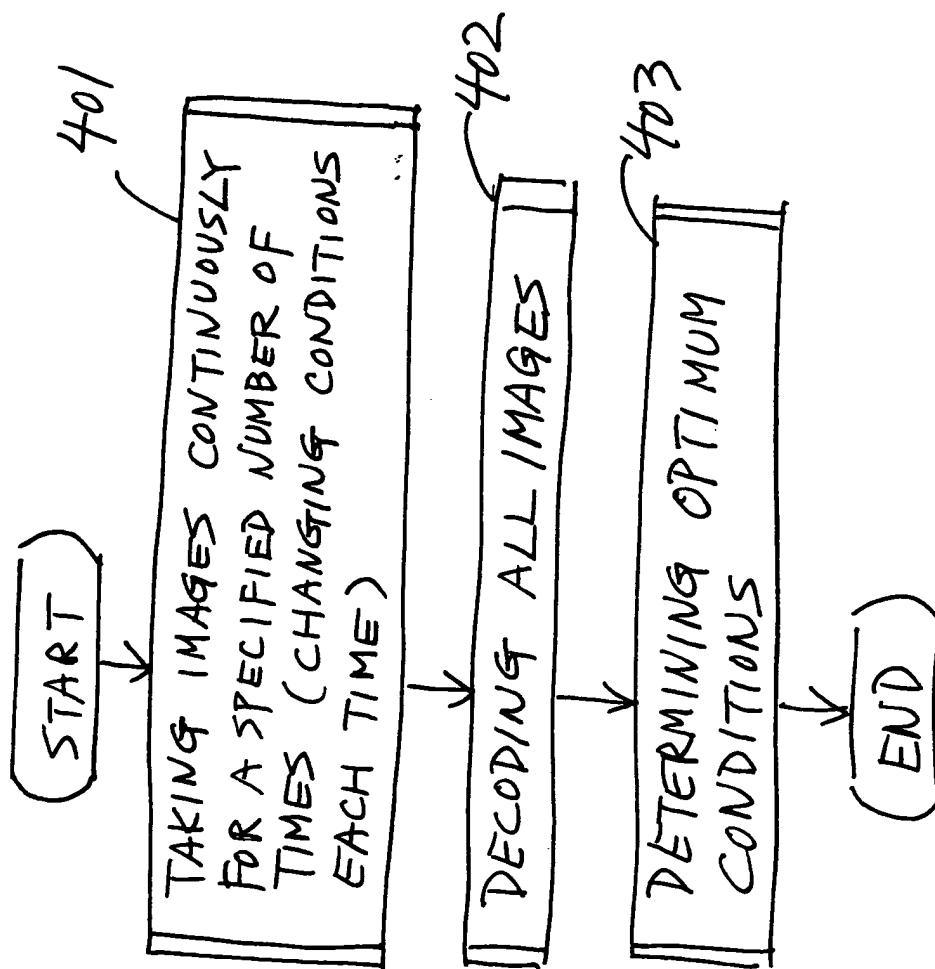
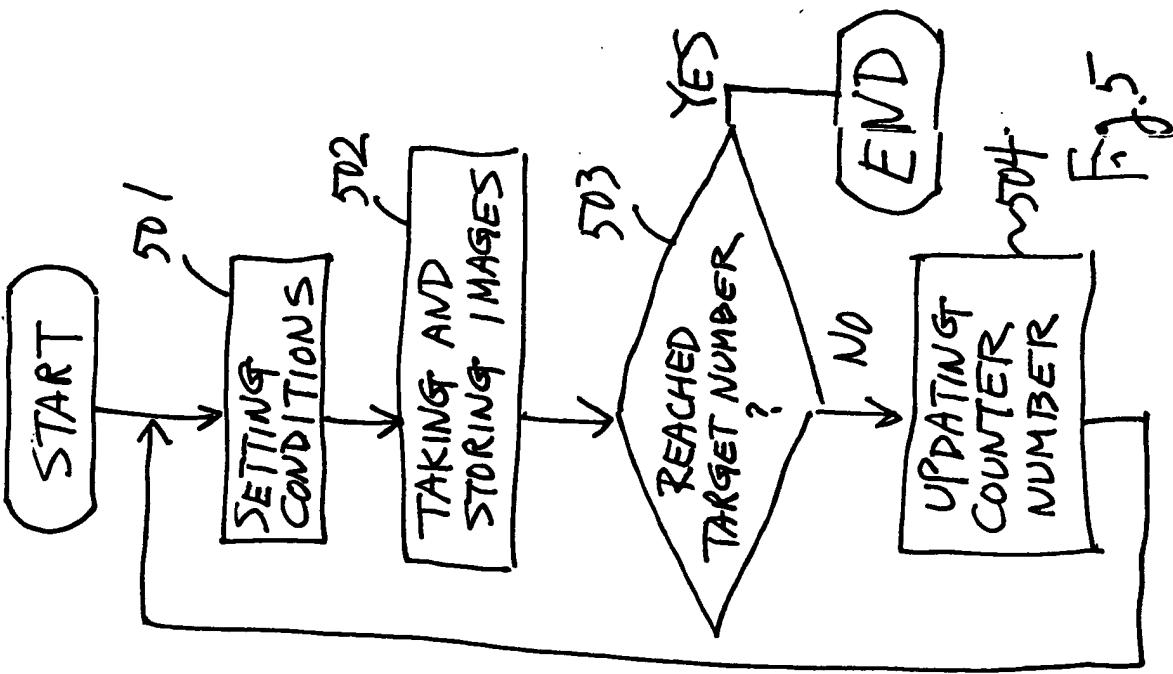


Fig. 4

Fig. 5

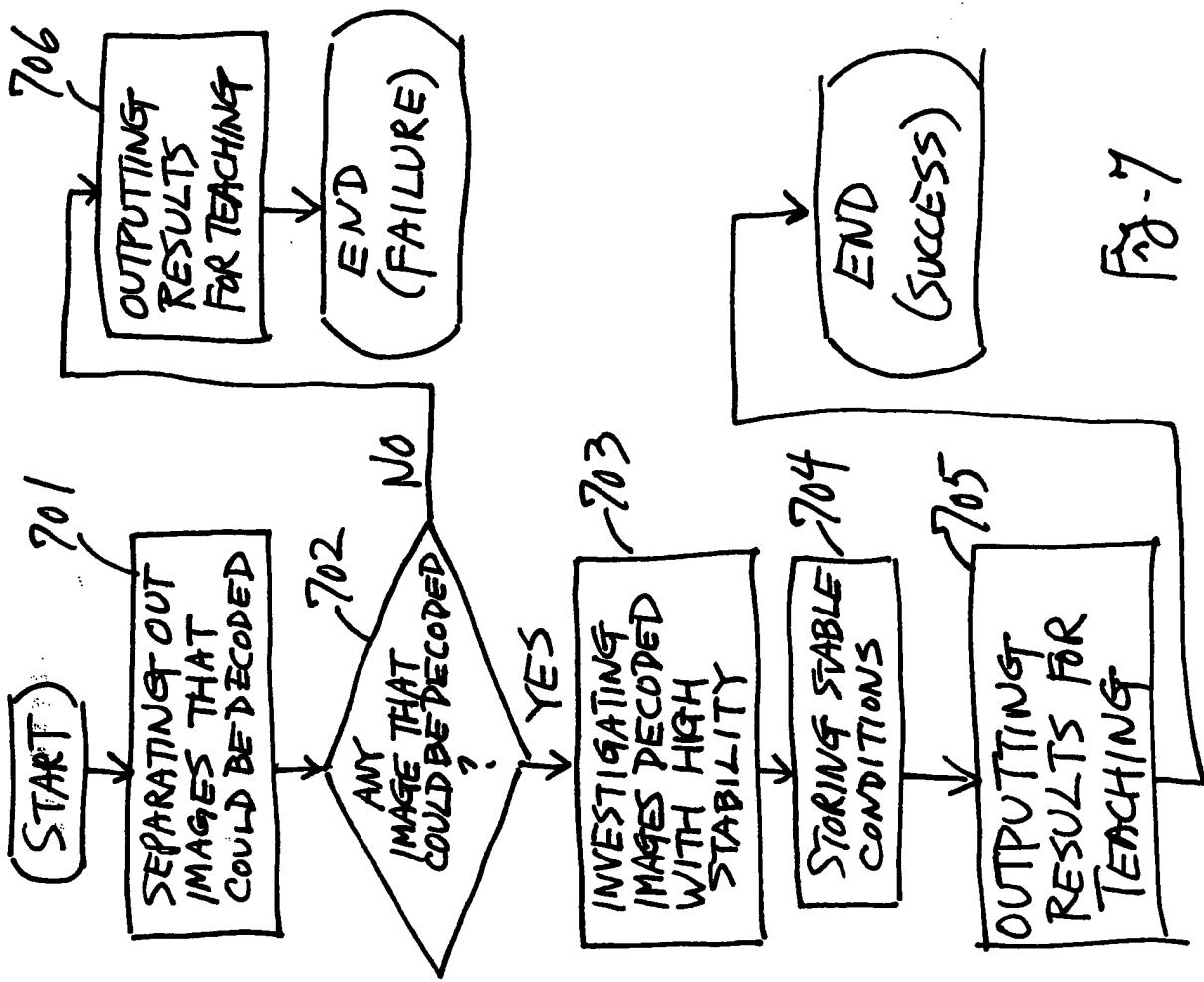


Fig. 7

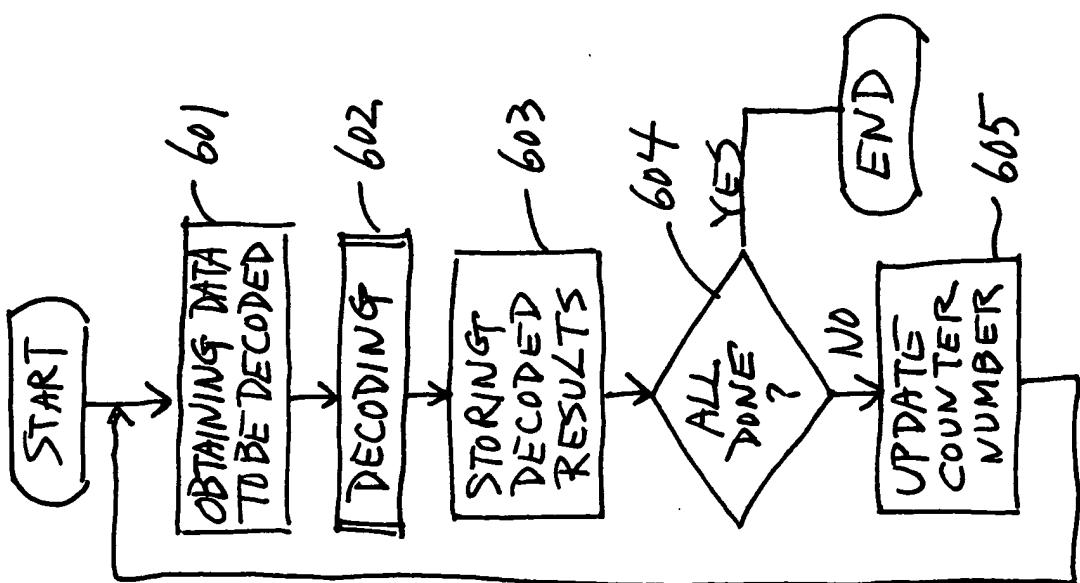


Fig. 6

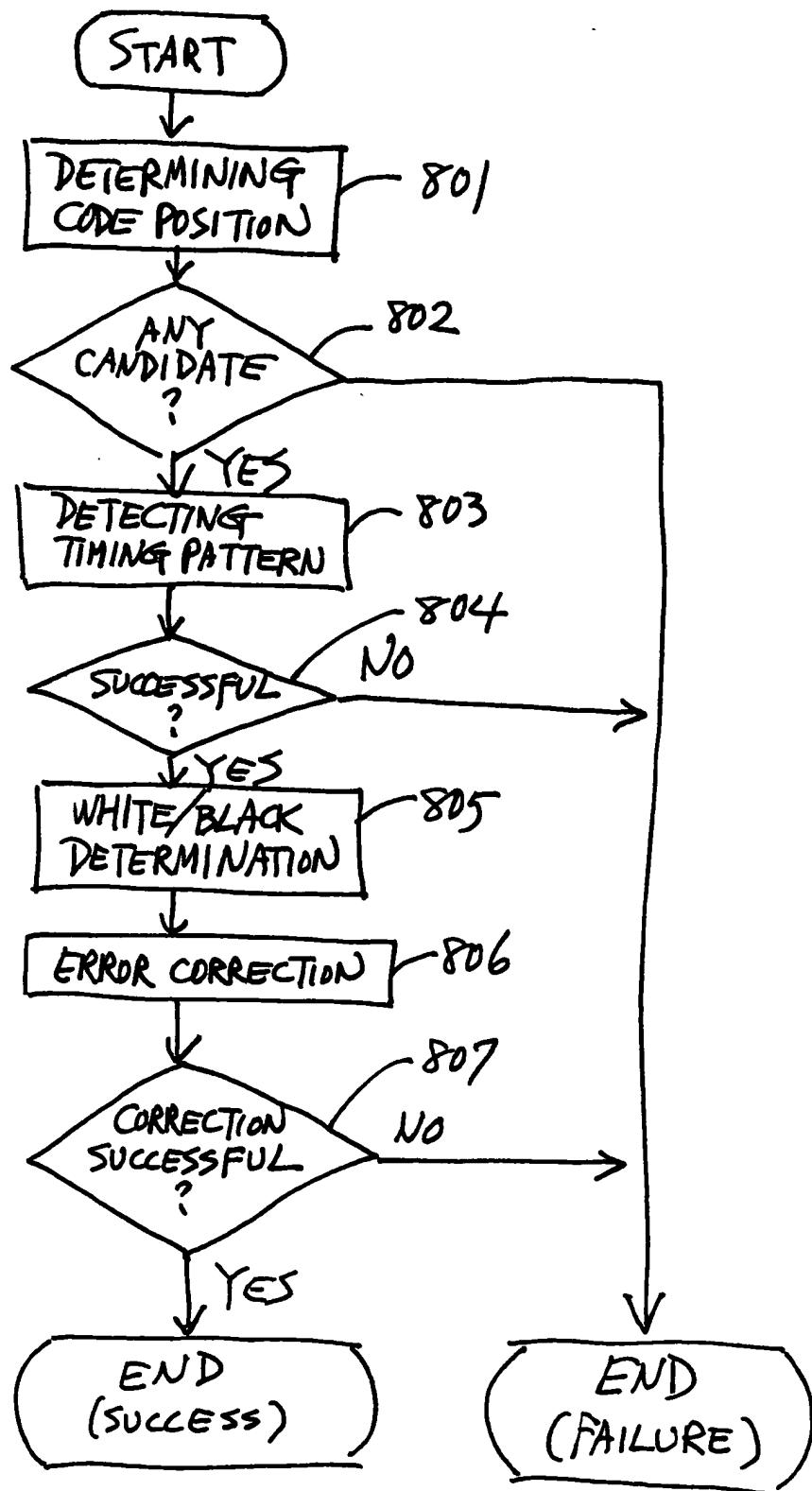


Fig. 8

TEACHING: SUCCESSFUL

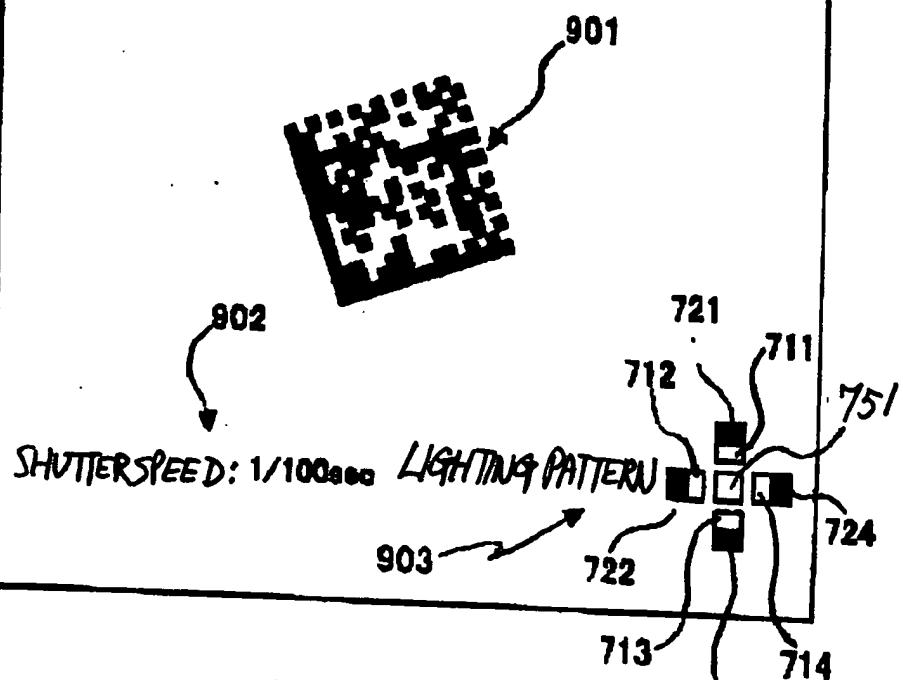


Fig. 9

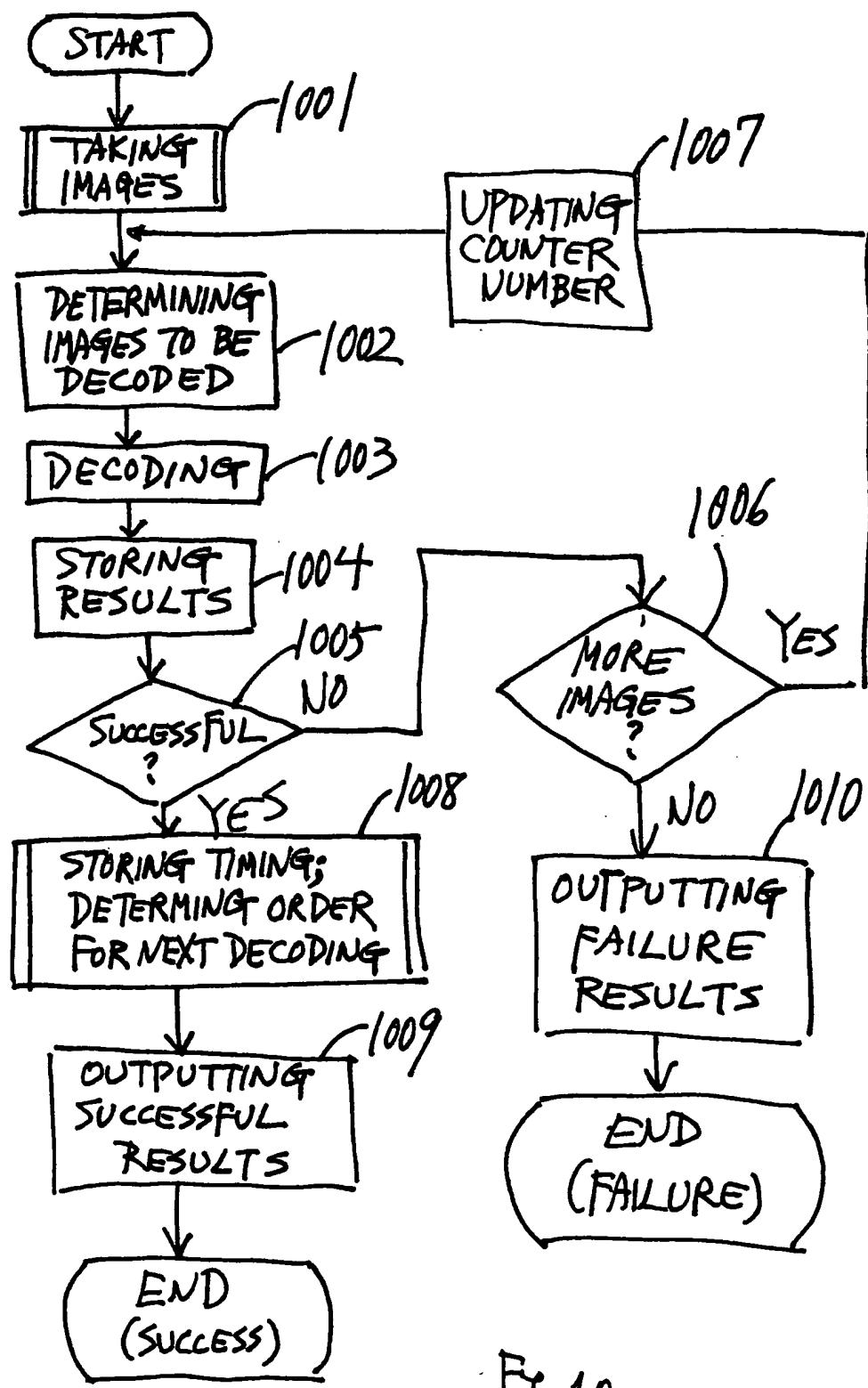


Fig.10

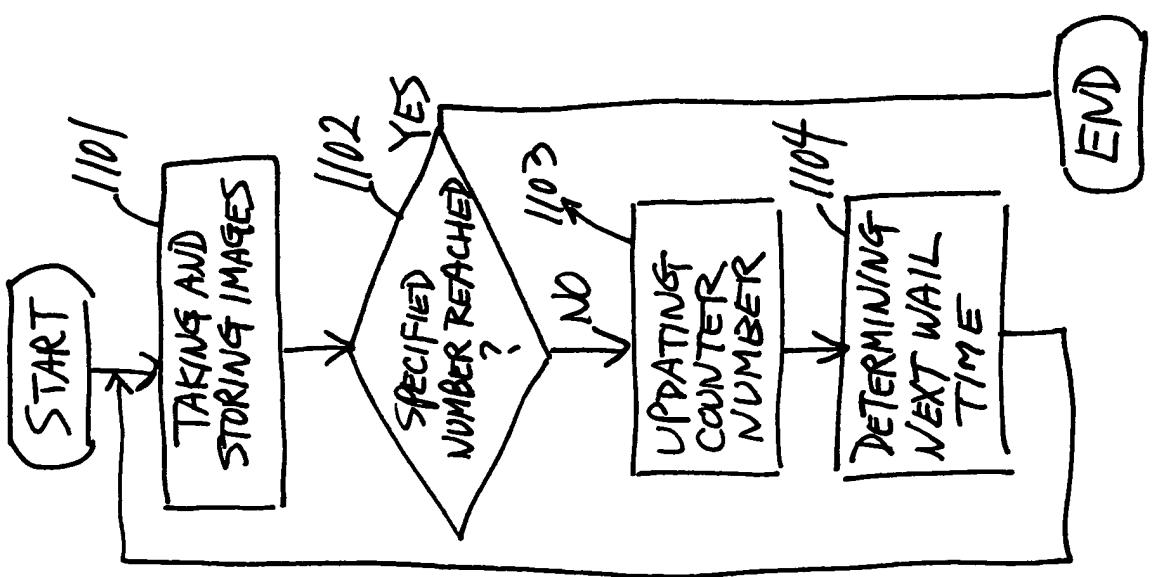
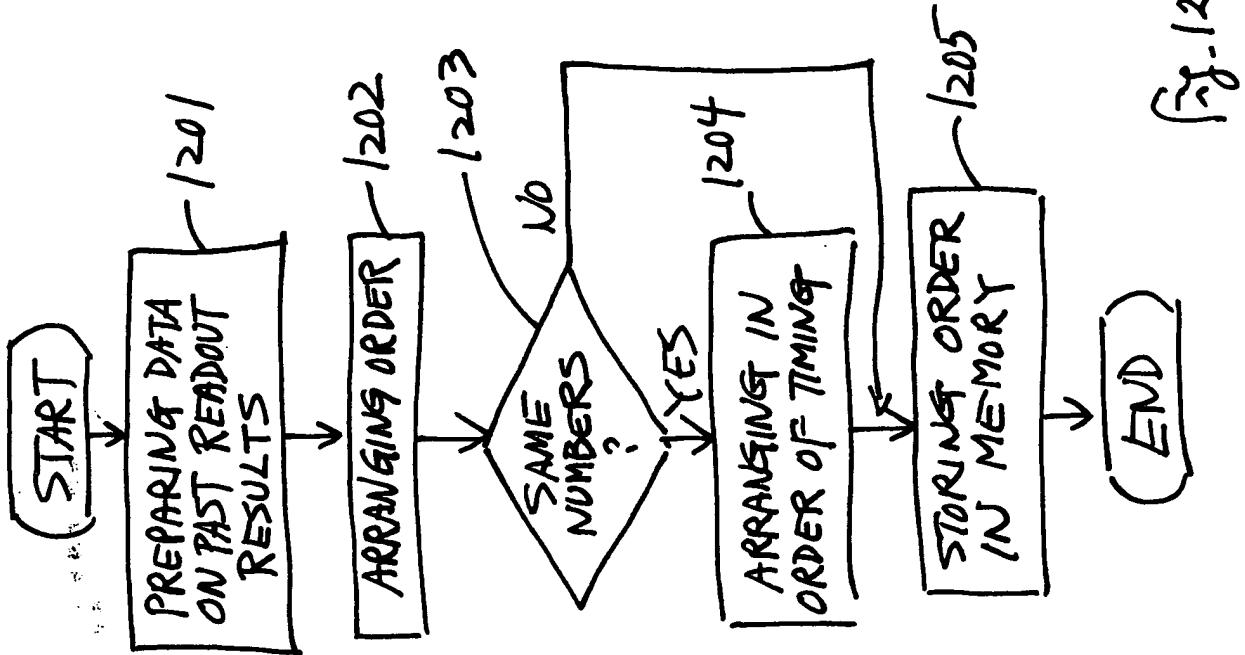


IMAGE No.	RESULT OF PAST READ OUT							TOTAL Success
	100 <sup>TH</sup>	99 <sup>TH</sup>	98 <sup>TH</sup>	97 <sup>TH</sup>	96 <sup>TH</sup>	95 <sup>TH</sup>	94 <sup>TH</sup>	
1								0
2								0
3								1
4	FAILURE	FAILURE	SUCCESS					9
5	SUCCESS	SUCCESS						85
6								5
7								0

Fig. 13

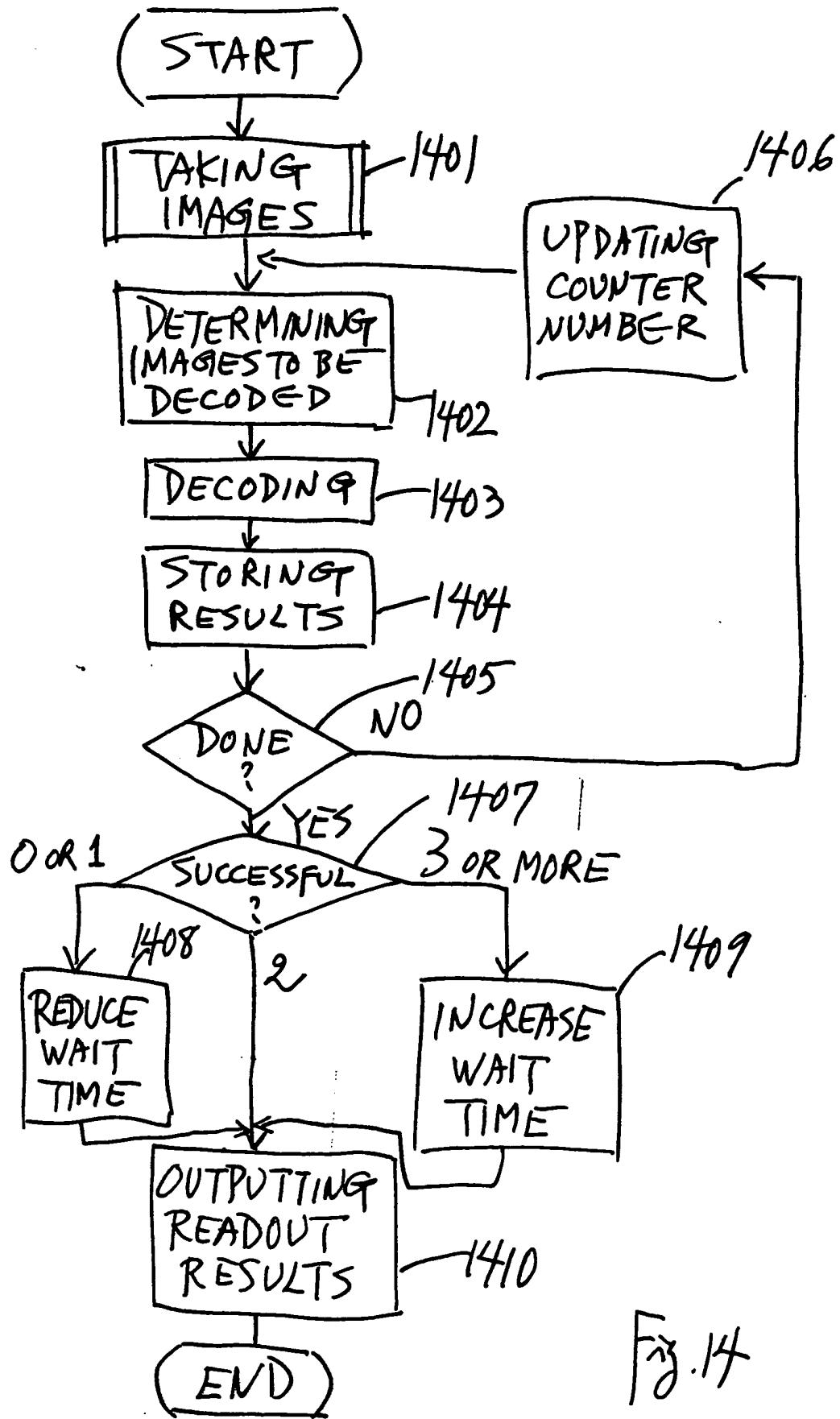


Fig.14

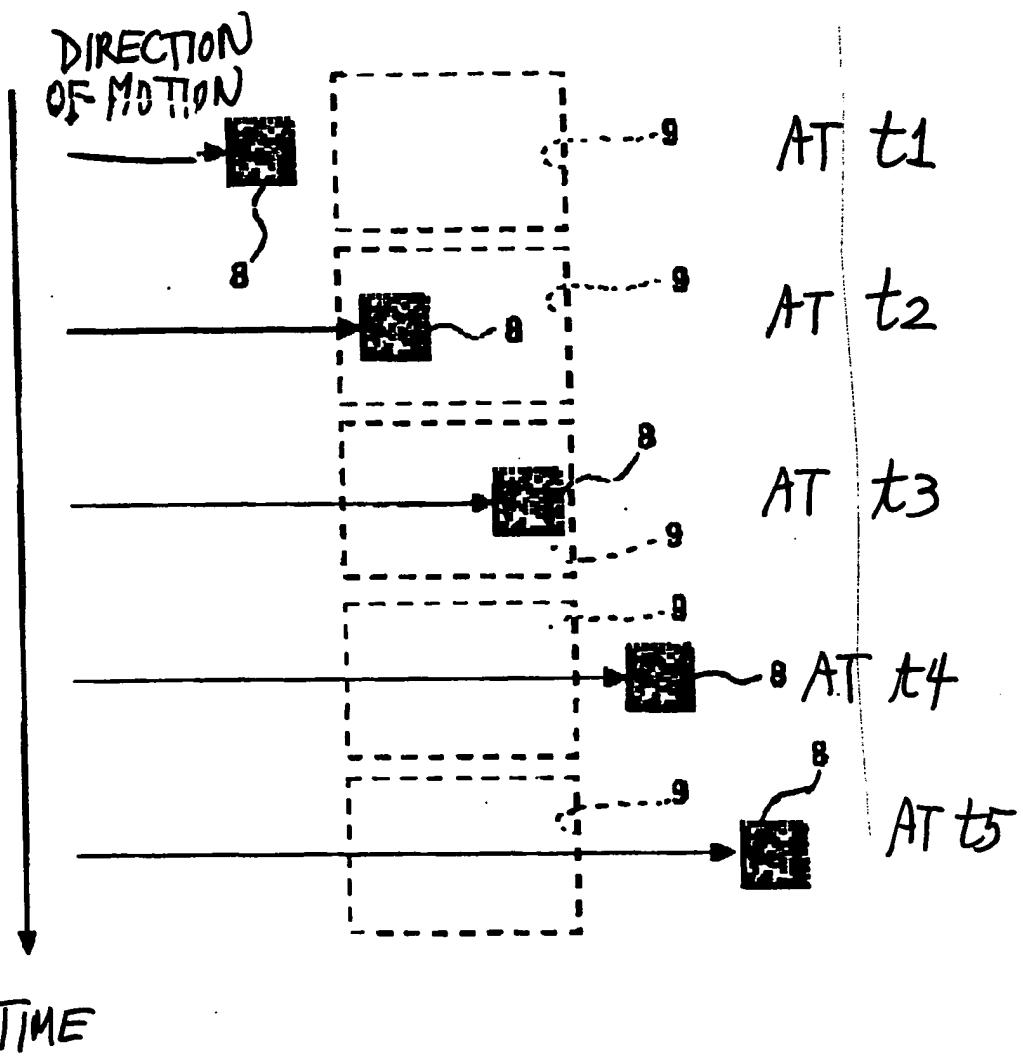


Fig. 15

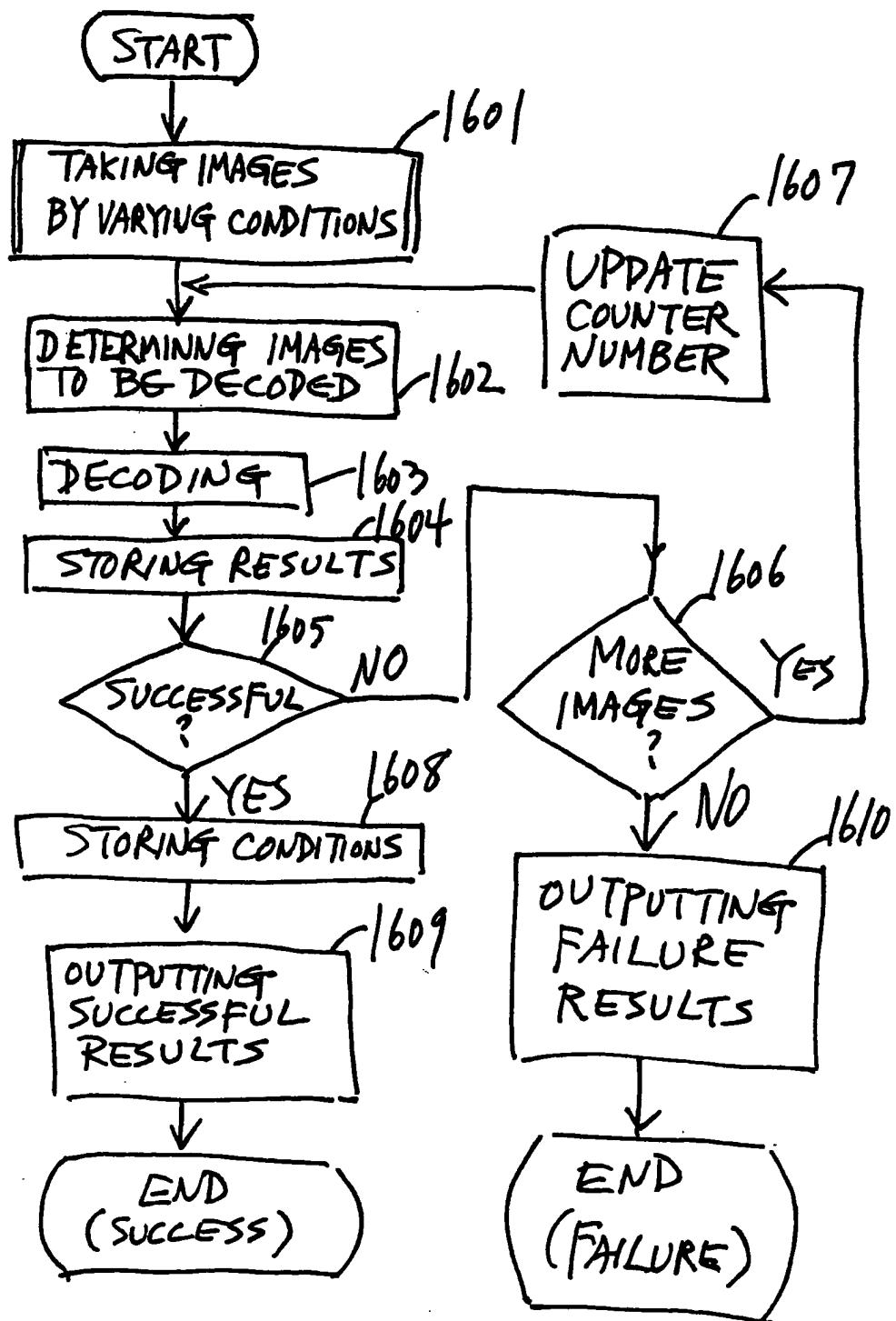


Fig.16

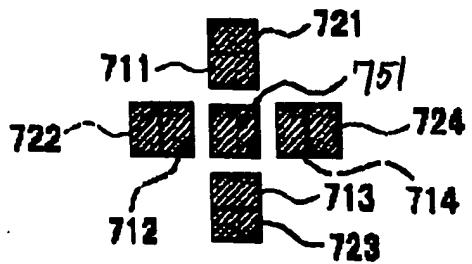
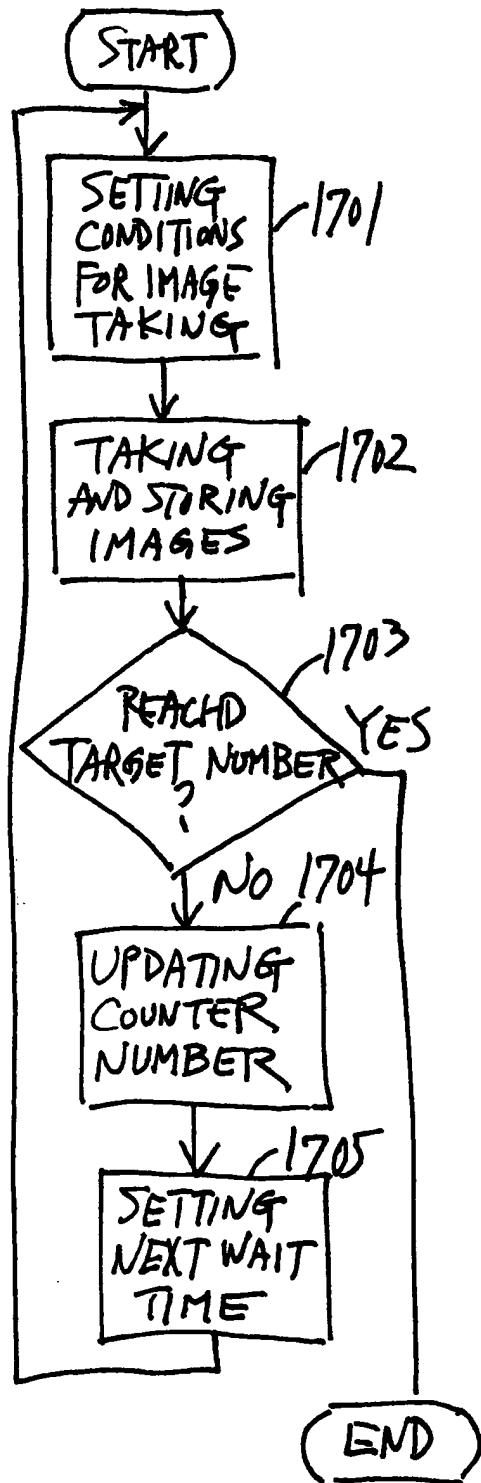


Fig. 18A

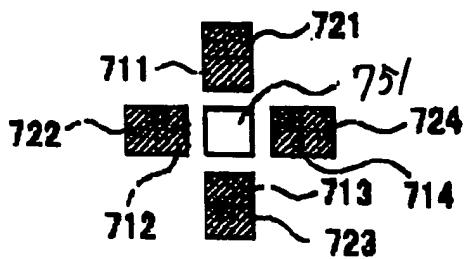


Fig. 18B

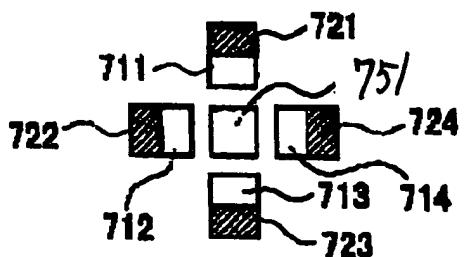


Fig. 18C

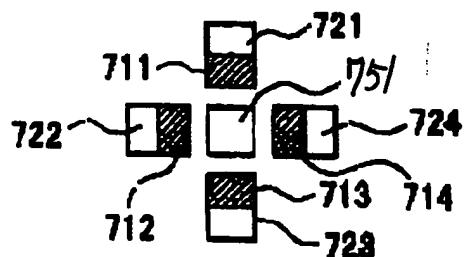


Fig. 18D

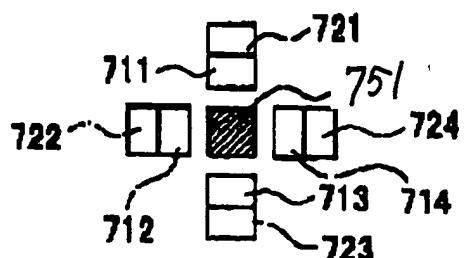


Fig. 18E

Fig. 17

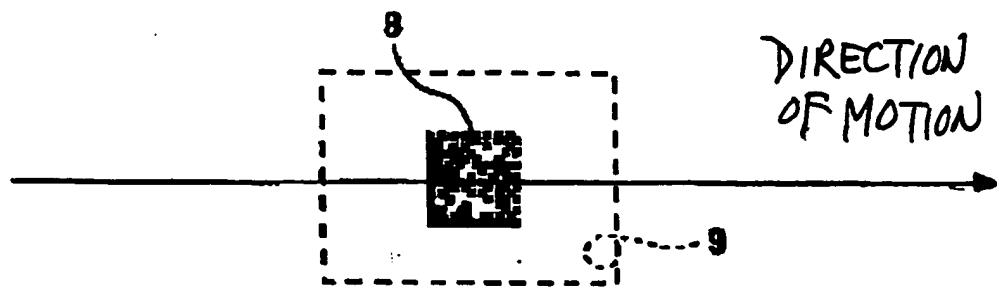


Fig. 19A  
(PRIDR ART)

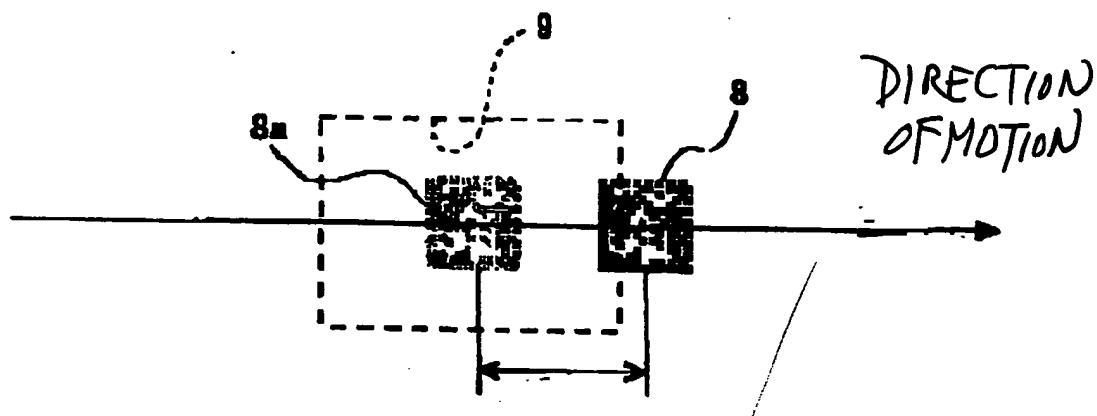


Fig. 19B  
(PRIOR ART)